

Notice of References Cited	Application/Control No. 09/867,375	Applicant(s)/Patent Under Reexamination BERTSCH ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Van Zant, P. Microchip Fabrication: A Practical Guide to Semiconductor Processing. ISBN 0-07-135636-3. © 2000. Chapter 15.
	V	Rajsuman, R. System-on-a-Chip: Design and Test. ISBN 1-58053-107-5. © 2000. Chapters 4 and 9-11.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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